

characterized in that the resonant circuits (10;20;30) are magnetically fixedly coupled to each other, and in that all the resonant circuits (10;20;30) of the circuit arrangement (100) are arranged on only one metallization plate (40) of an integrated circuit, having an essentially constant ohmic resistance, said metallization plate being one of plural, and being the one having the least ohmic resistance.

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2. (Cancelled)
  3. (Currently Amended) A circuit arrangement as claimed in claim 1, characterized in that the individual resonant circuits (10;20;30) are essentially arranged in a planar way on an outer surface area of the integrated circuit, and being arranged as concentrically aligned inductors.
  4. (Cancelled)
  5. (Previously Amended) A circuit arrangement as claimed in claim 4, characterized in that the geometric structure is a circle, an oval, an ellipse, a square or a rectangle.
  6. (Cancelled)
  7. (Currently Amended) A circuit arrangement as claimed in claim 1, characterized in that at least two inductive elements (12;22;32) which are substantially concentric and/or substantially parallel to each other. Said capacitive elements are arranged linearly with respect to each other and number at least three.
  - 8-11. (Cancelled)
  12. (Previously Added) A circuit arrangement as claimed in claim 1, characterized in that the inductive elements (12;22;32) are magnetically fixedly coupled to each other.
  13. (Previously Added) A circuit arrangement as claimed in claim 7, characterized in that each of the inductive elements (12;22;32) comprises one turn.